

## BSEC Binary Size Information

**BSEC version: 2.0.6.1**

### 1. Platform Supported Currently

Platform	Compiler	TYPE
Cortex-ARM	Keil5 ARMCC	Cortex-M0, M0+, M3, M4, M4F, M7
Cortex-ARM	arm-none-eabi-gcc	Cortex-M0, M0+, M3, M33, M33F, M4, M4F, M7, ARMv8-m
Cortex-A*	arm-none-eabi-gcc	Cortex-A7, A73
AVR_8bit	Atmel Studio AVR-GCC	MegaAVR, XMEGA
AVR_32bit	Atmel Studio AVR-GCC	32-bit AVR UC3
ESP	xtensa-lx106-elf-gcc	ESP8266, ESP32
MSP430	mcp430-elf-gcc	MSP430
IAR	IAR compiler	Cortex-M0, M0+, M3, M4, M4F, M7
Raspberry pi	Arm-linux-gnueabi-gcc	armv6, armv8-a
Windows	TDM-GCC	x86, x64
Linux	GCC	x86, x64

### 2. Binary Size on different platform

Platform Type	Compiler	ROM(.text+.data) in bytes	RAM(.data+.bss) in bytes
Cortex-M0	Keil5 ARMCC	25254	4388
Cortex-M0+		25254	4388
Cortex-M3		24228	4388
Cortex-M4		24228	4388
Cortex-M4F		25722	4388
Cortex-M7		24230	4388
Cortex-M0		arm-none-eabi-gcc	30009
Cortex-M0+	30009		4388
Cortex-M3	27317		4388
Cortex-M33	27337		4388
Cortex-M33F	49453		4388
Cortex-M4	27225		4388
Cortex-M4F	27841		4388
Cortex-M7	27865		4388
ARMv8-m	29373		4388

Cortex-A7		28209	4388
Cortex-A73		33005	4388
AVR8bit-MegaAVR	Atmel Studio AVR-GCC	57863	3961
AVR8bit-XMEGA		56825	3961
AVR 32bit		31992	4500
ESP32	Elf-gcc	31844	4396
ESP8266		36247	4396
Msp430		45006	3974
Armv6	Arm-linux-gnueabihf-gcc	75368	4396
Armv8-a		75388	4396
Cortex-M0	IAR7	27474	4388
Cortex-M0+		27474	4388
Cortex-M3		27052	4388
Cortex-M4		27052	4388
Cortex-M4F		27734	4388
Cortex-M7		27052	4388
Cortex-M0	IAR8	27792	4388
Cortex-M0+		27792	4388
Cortex-M3		27004	4388
Cortex-M4		27002	4388
Cortex-M4F		27620	4388
Cortex-M7		27002	4388
Windows_x64	TDM-GCC	46936	4480
Windows_x86		44720	4480
Linux_m64	GCC	51949	4432
Linux_m32		65039	4408

\*Note:

1. ROM/RAM size is basic requirement of BSEC. Static Lib File size doesn't count.
2. M4F/M33F means the MCU with FPU.